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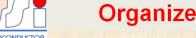
International Symposium on Semiconductor Materials and Devices-2022

ISSMD-2022

16th – 18th December 2022
(https://kiit.ac.in/event/international-symposium-on-semiconductor-materials-and-devices-2022/)

Organized by

School of Electronics Engineering, KIIT-Deemed to be University and Semiconductor Society of India in co-operation with Society of Semiconductor Devices

Scope of the Conference

It is our pleasure to announce the "International Symposium on Semiconductor Materials and Devices-2022" to be held at KIIT Deemed to be University, Bhubaneswar, India from 16th December to 18th December 2022 jointly organized by School of Electronics Engineering, KIIT and Semiconductor Society of India (SSI). The symposium is being organized to bring the various researchers working in the field of semiconducting materials and devices on a common platform. There will be discussions on the recent advances in the field. The participants can interact with the various experts of the field which will bring together the researchers from various fields. It will lead to exchange of ideas and help in developing new ones. The present Conference provides a platform to exchange knowledge about the latest scientific and industry results and to discuss the latest trends and developments in the area of semiconductor.

Semiconductor Society (India)

Semiconductor Society (India) is a not for profit professional body serving Semiconductor Community for about four decades. It currently has more than seven hundred members. It undertakes a variety of regular professional activities, including lectures, seminars, conferences etc., aimed to promote mutual interaction of scientists and technologists working in the field of Semiconductor Materials and Devices.

We aim to provide members with a platform where they can easily share information about the advances in semiconductor technologies. Our forum is made up of a community of talented and forward-thinking individuals who are always striving to learn or teach something new to those around them.

Join our Technology+Community to stay relevant in the ever-changing world of technology, in which each day means a whole new world of unimagined possibilities.

Topics to be covered:

1. Wide band gap semiconductors
2. Organics and amorphous semiconductor
3. Crystal growth and epitaxy of semiconductor materials
4. Semiconductor Nanostructure and Devices
5. Photovoltaic Devices
6. Semiconductors for Quantum Technologies
7. Semiconductor device modeling and simulation

KIIT DEEMED TO BE UNIVERSITY

Kalinga Institute of Industrial Technology (KIIT) was established in 1992 as a vocational training centre with only two staff and 12 students. However, it took shape as an institution only in 1997, when degree engineering programme commenced. KIIT owes its existence to the vision of its founder Prof. Achyuta Samanta who, with no land, limited finances (only Rs. 5000/-) and little knowledge of higher education to create an institution for the benefit of the society. From these small beginnings, KIIT has developed into a leading university with a strong interdisciplinary culture and a commitment to teaching excellence.KIIT is one of the first institutions in the State of Odisha to attract foreign students into its various academic programmes. KIIT has students from over 60 countries, including most of the South Asian Nations, proving its reputation as a quality institution of professional education that transcends borders (http:// www.kiit.ac.in). Its protégé, KISS Foundation is an NGO in India headquartered in Bhubaneswar, Odisha. School, College and University is the educational wing of this initiative located at the intersection of food, education and empowerment. We are powered by a belief that a better world – free from hunger, poverty & illiteracy – is possible.

SCHOOL OF ELECTRONICS ENGINEERING

School of Electronics Engineering aims to equip its students to be able to lead a successful career in industry, pursue higher studies in reputed Indian and foreign institutions or embark on entrepreneurial endeavors. The classroom studies are reinforced in well-equipped laboratories, which help students in experiential learning. The School has a strong and dedicated team of faculty members drawn from premier institutes such as IITs and NITs. Apart from classroom teaching, faculty members are involved in publication and interaction with outside world on a frequent basis. It conducts regular workshops, conferences, seminars and invited talks. The School is involved in teaching and research activities in the fields of Telecommunication, Microelectronics and VLSI, Signal Processing, RF and Microwave, Instrumentation Engineering, Semiconductors, Renewable Energy, etc. The Centre for Innovation & Research fosters overall growth by facilitating interactions with external agencies, and promoting and managing interactions between the school and the industry. The School is presently implementing numerous projects sponsored by Dept. of Atomic Energy, DRDO, DST, AICTE, etc. Some of the recent sponsored projects are in the area of photovoltaic. The School has following research thrust areas: AI/ML; Signal and Image Processing; Signal Processing; Solar Energy Photovoltaic; VLSI and Embedded Systems; Electronics Devices; Wireless and Mobile Communication; Optical Communication; Internet of Things; Cloud, Media and Applications.

Calendar and Deadline

➢ **Abstract submission** : 10th September, 2022
➢ **Acceptance of abstract** : 26th September, 2022
➢ **Pre registration** : 15th October, 2022
➢ **Full Paper Submission *** : 20th October, 2022

Registration fee

❑ **Faculty** : Rs. 4000/-
❑ **Research Scholar** : Rs. 3500/-
❑ **Undergraduate/PG Student** : Rs. 2500/-
❑ **Industry** : Rs. 5000/-
❑ **Foreign Delegates** : USD 200/-

*Publishing the paper in peer reviewed journal is in progress. Detail will be updated soon

Call For Paper

Abstract should be limited to 300 words (excluding title, name, designation, affiliations, and keywords). Page set up: A4 paper, margin: left (3cm), right (2cm), top (3cm) and bottom (3cm). The title must be less than 200 characters, bold 14 point times new roman and in center. For the text of abstract, 10 point roman in sentence case should be in justified alignment with the single line spacing followed by maximum of 5 keywords. Abstract/ full length paper should have upload only through designated link (to be updated in the website).

Conference Registration Link

Will be updated in the website soon.

Plagiarism Policy

Authors should submit their original and unpublished research work, which is not under consideration for publication elsewhere. Authors are also requested to kindly refrain the full length paper from plagiarism. Manuscripts found to be plagiarized during any stage of review will be rejected.

Bhubaneswar

Bhubaneswar, an ancient capital of Kalinga Empire, has a greatest architectural legacy of temples like Jagannath, Lingaraj, Mukteswar, Brahmeswar, etc. from 7th to 11th century A.D., which is one of the greatest attractions of tourism. Apart from the temples, the caves of Khandagiri and Udayagiri, Nandankanan zoological park, Kanjia Lake, etc. are also the attractions of visitors.